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Substitute for form 1449A/PTO INFORMATION DISCLOSURE

STATEMENT BY APPLICANT

Complete if Known Application Number 10/646,013 Filing Date 8/22/2003 First Named Inventor Cheng H. Huang Art Unit 2826 **Examiner Name** Remmon R. Forde

Attorney Docket Number | A1063 Sheet

U. S. PATENT DOCUMENTS						
Examiner Initials*	Cite No.1	Document Number Number-Kind Code ^{2 (I known)}	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
1.9		us- 4,207556	06-10-1980	Yoshi et al.		
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Cite No.1	Foreign Patent Occurrent Country Cods ³ Number ⁴ Mind Code ⁵ (# known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	Τ°		
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		Cite Foreign Patent Document	Cite Foreign Patent Occurrent Publication Date	Cite Foreign Patent Document Publication Date Name of Patentee or No. 1	Cite Foreign Patent Document Publication Date Name of Patentee or Pages, Columns, Lines, No. 1		

Examiner	9/	121-	Date	1.110111
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Substitute for form 1449B/PTO Complete if Known Application Number 10/646,013 **INFORMATION DISCLOSURE** Filing Date 8/22/2003 STATEMENT BY APPLICANT First Named Inventor Cheng H. Huang Art Unit 2826 (Use as many sheets as necessary) Examiner Name Remmon R. Forde Attorney Docket Number 2 2 A1063

NON PATENT LITERATURE DOCUMENTS							
Examiner Cite No.		Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.					
1.1		WEI ZHANG ET AL., "Energy Effect of the Laser-Induced Vertical Metallic Link", IEEE Transactions on Semiconductor Manufacturing, Vol 14, No. 2, May 2001 pp 163-169					
1		"Analyzing the process window for laser copper-link processing" Solid State Technology - Semiconductor manufacturing and wafer fabrication, Jan 8, 2003					
	*******	ALEXANDER KALNITSKY et al. "CoSi2 integrated fuses on poly silicon for low voltage 0.18 um CMOS applications" (c) 1999 IEEE					
		MOHSEN ALAVI "A PROM Element Based on Salicide Agglomeration of Poly Fuses in a CMOS logic process" IEEE International Electron Devices Meeting, December 1997					
		NORIAKI SATO et al. "A New Programmable Cell Utilizing Insulator Breakdown", IEDM 1985, pp 639-642					
	**********	JINBONG KIM et al. "Three-Transistor One-Time Programmable (OTP) ROM Cell Array Using Standard CMOS Gate Oxide Antifuse", IEEE Electron Device Letters, Vol 24, No. 9, September 2003 pp. 589-591					
1.5.		WEI ZHANG et al. "Laser-Formed Vertical Metallic Link and Potential Implementation in Digital Logic Integration", PROC MAPLD, MD, 1999 pp 1-7 (B5)					

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Examiner Signature	1	-//	Falo	Date Considered	11/12/04
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